

# North Hennepin Community College

## ENGR 2201: Introduction to Digital Logic

### A. COURSE DESCRIPTION

Credits: 2

Lecture Hours/Week: 0

Lab Hours/Week: 0

OJT Hours/Week: \*.\*

Prerequisites:

This course requires any of these three prerequisites

Placement into MATH 1170 or MATH 1200

Placement into MATH 1221

MATH 1150 - College Algebra (Minimum grade: 1.67 GPA Equivalent)

Corequisites: None

MnTC Goals: None

This is an introduction to digital logic design. Topics include Boolean algebra fundamentals; Karnaugh mapping; elements of digital building blocks such as gates, flip-flops, shift registers, memories, etc.; analysis and design of combinational-logic circuit and sequential-logic circuits.

**B. COURSE EFFECTIVE DATES:** 06/15/2021 - Present

### C. OUTLINE OF MAJOR CONTENT AREAS

None

### D. LEARNING OUTCOMES (General)

None

### E. Minnesota Transfer Curriculum Goal Area(s) and Competencies

None

### F. LEARNER OUTCOMES ASSESSMENT

As noted on course syllabus

### G. SPECIAL INFORMATION

None noted